

FIG. 1 is a schematic diagram of a device 100, showing a top view of the device 100. The device 100 includes a substrate 11, a first layer 13, and a second layer 15. The first layer 13 is disposed on the substrate 11, and the second layer 15 is disposed on the first layer 13. The second layer 15 includes a first portion 15a and a second portion 15b. The first portion 15a is disposed on the first layer 13, and the second portion 15b is disposed on the first layer 13. The first portion 15a and the second portion 15b are separated by a gap 13a. The device 100 is shown with a length of 1000 and a width of 2000.

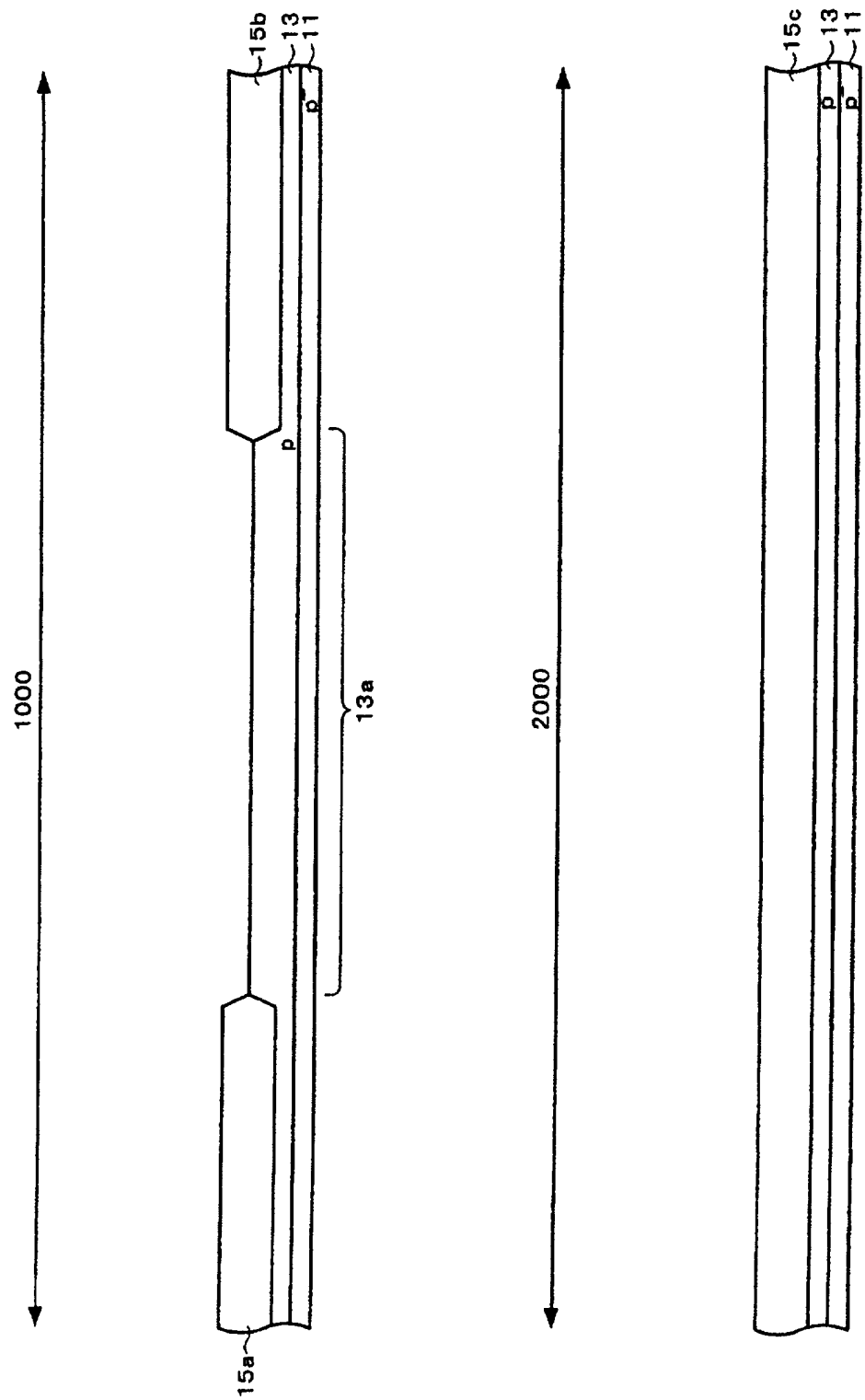


Fig. 1

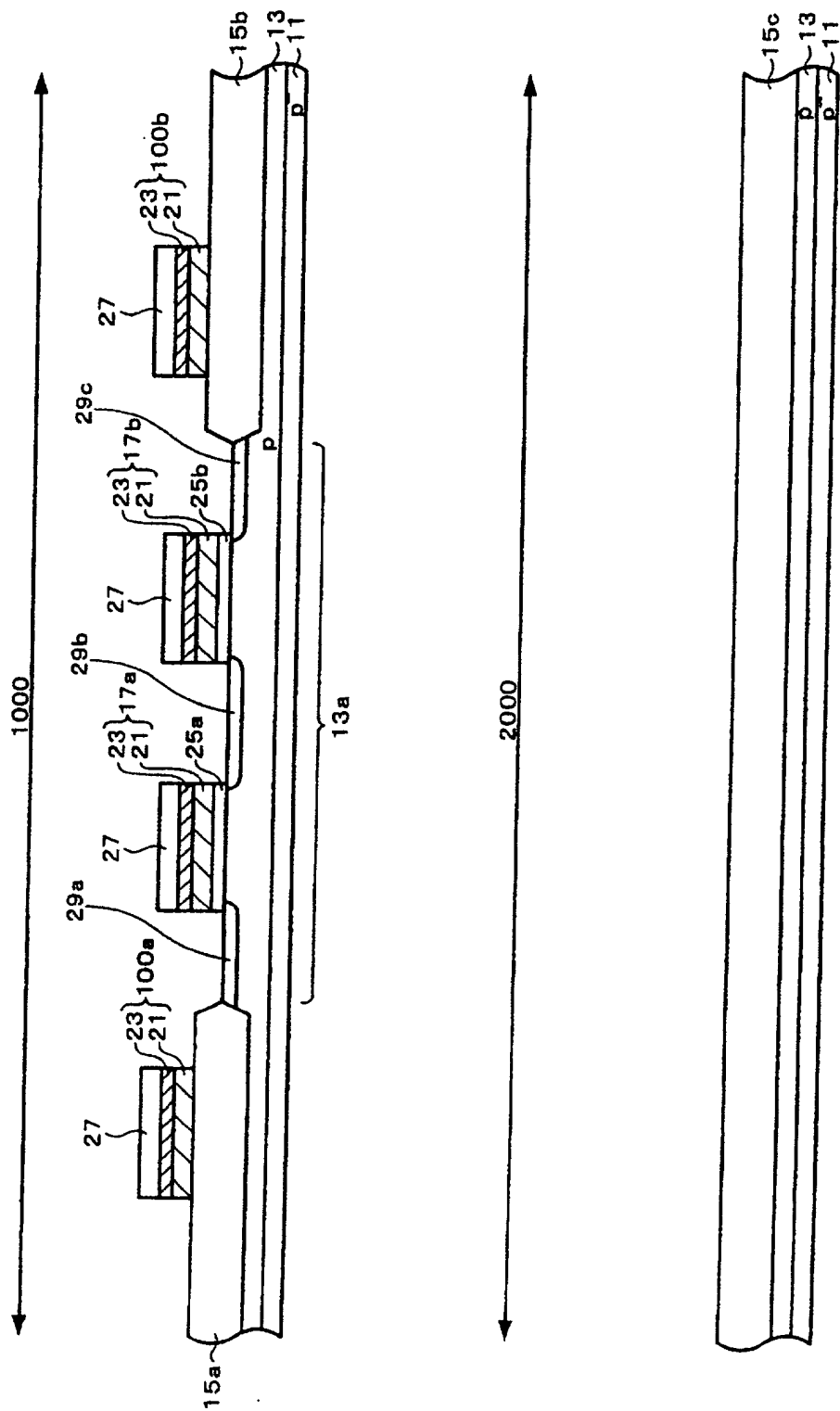
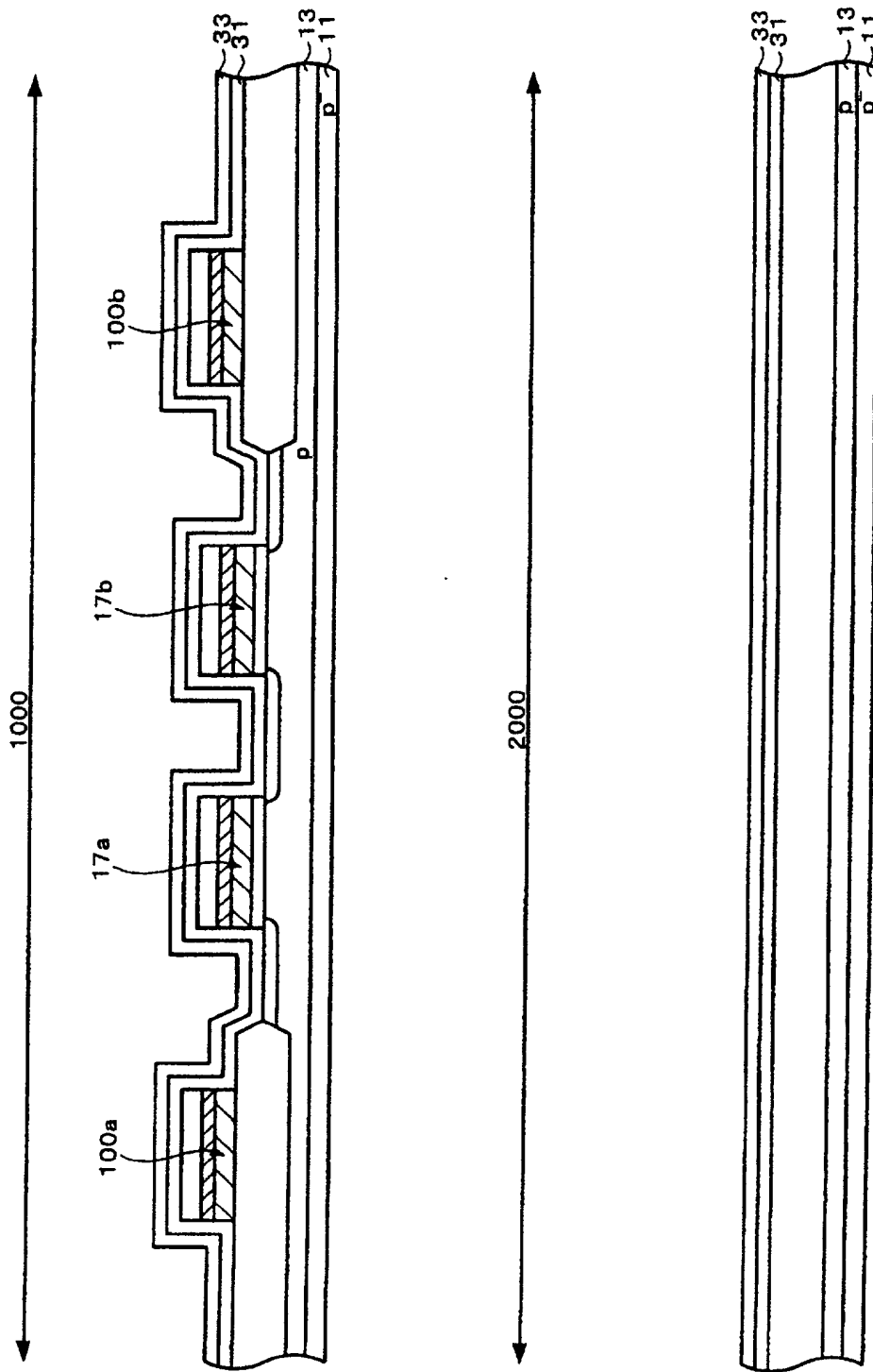
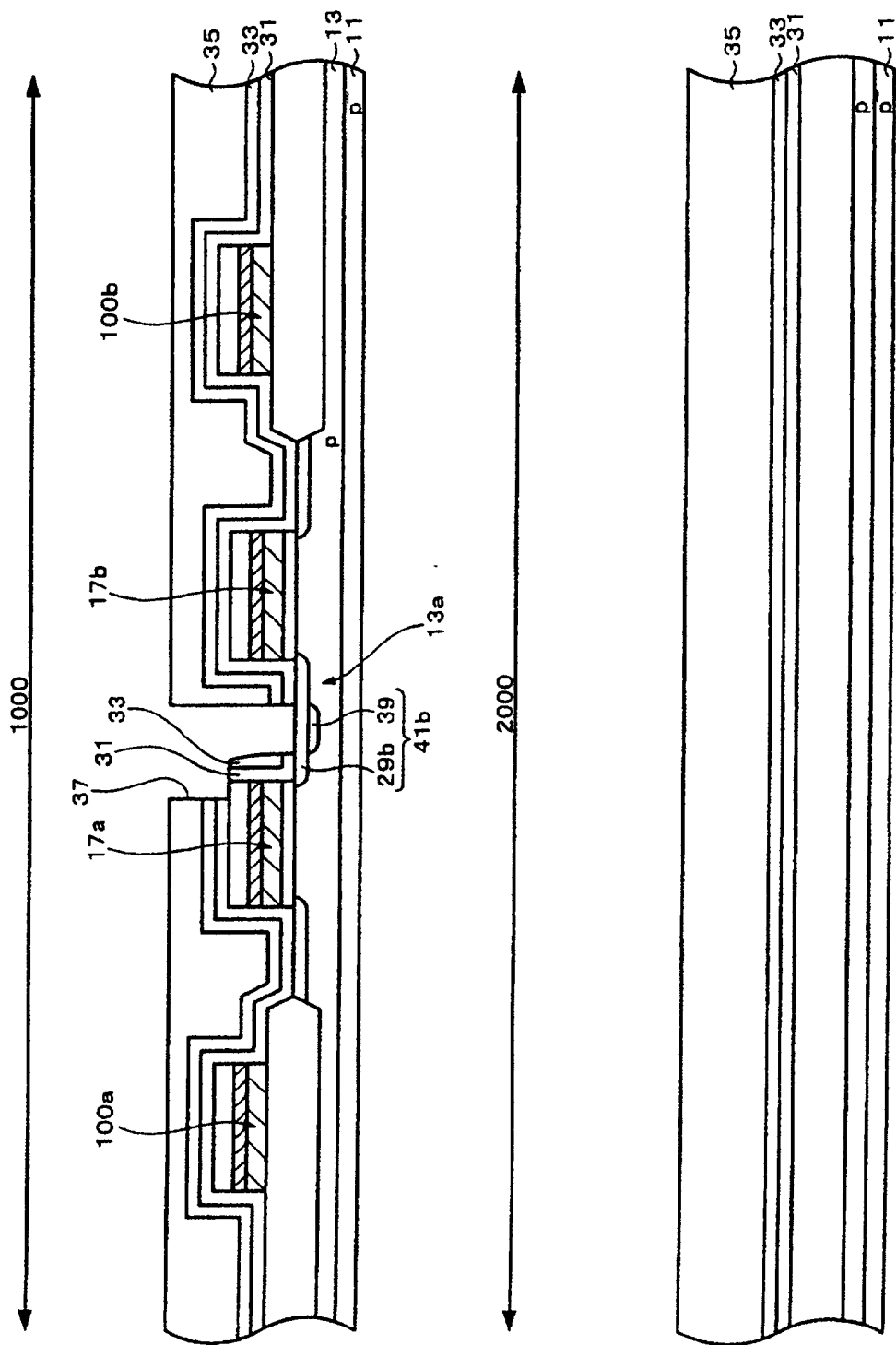


Fig. 2



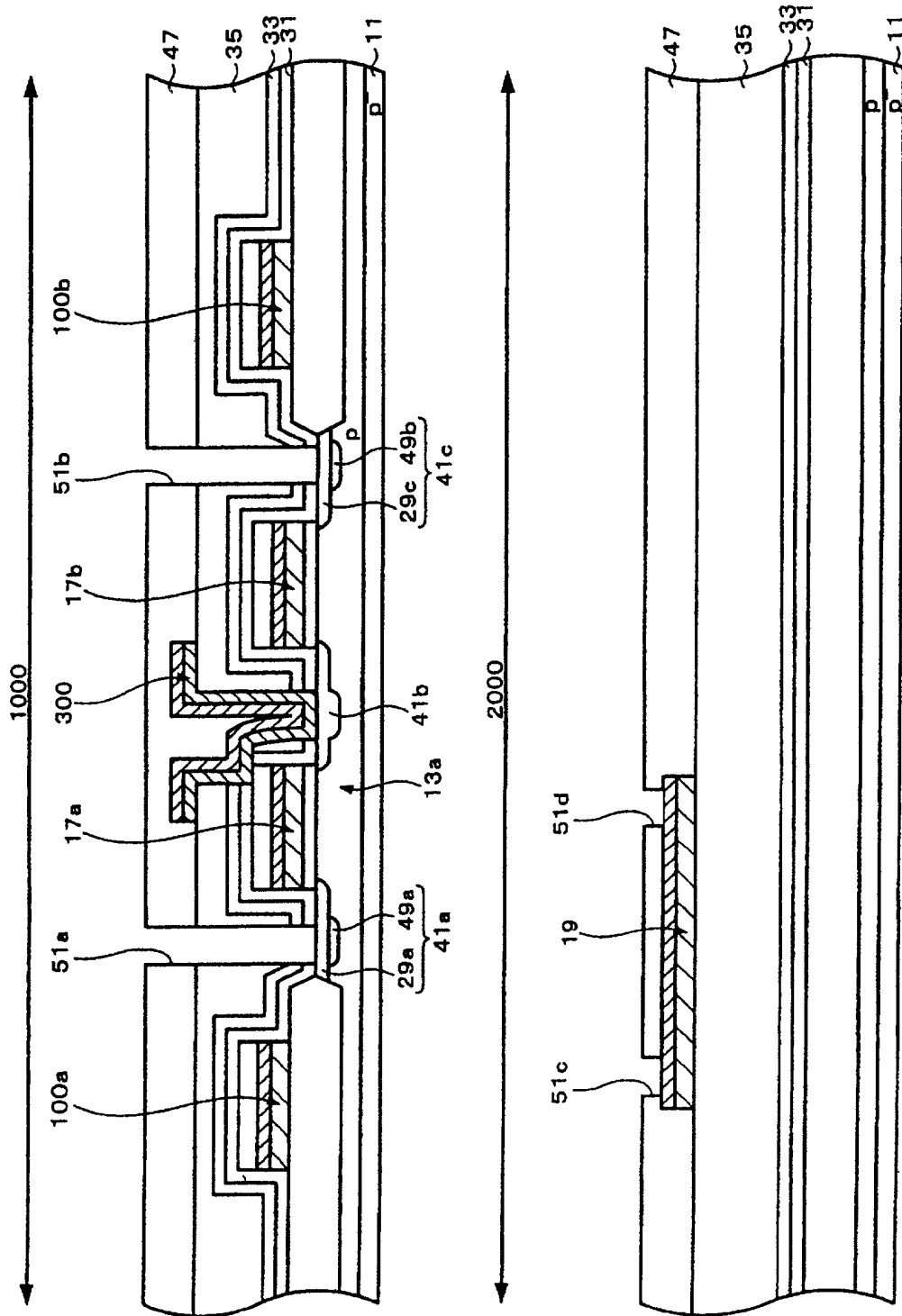
2000

Fig. 3



**Fig. 4**





**Fig. 6**

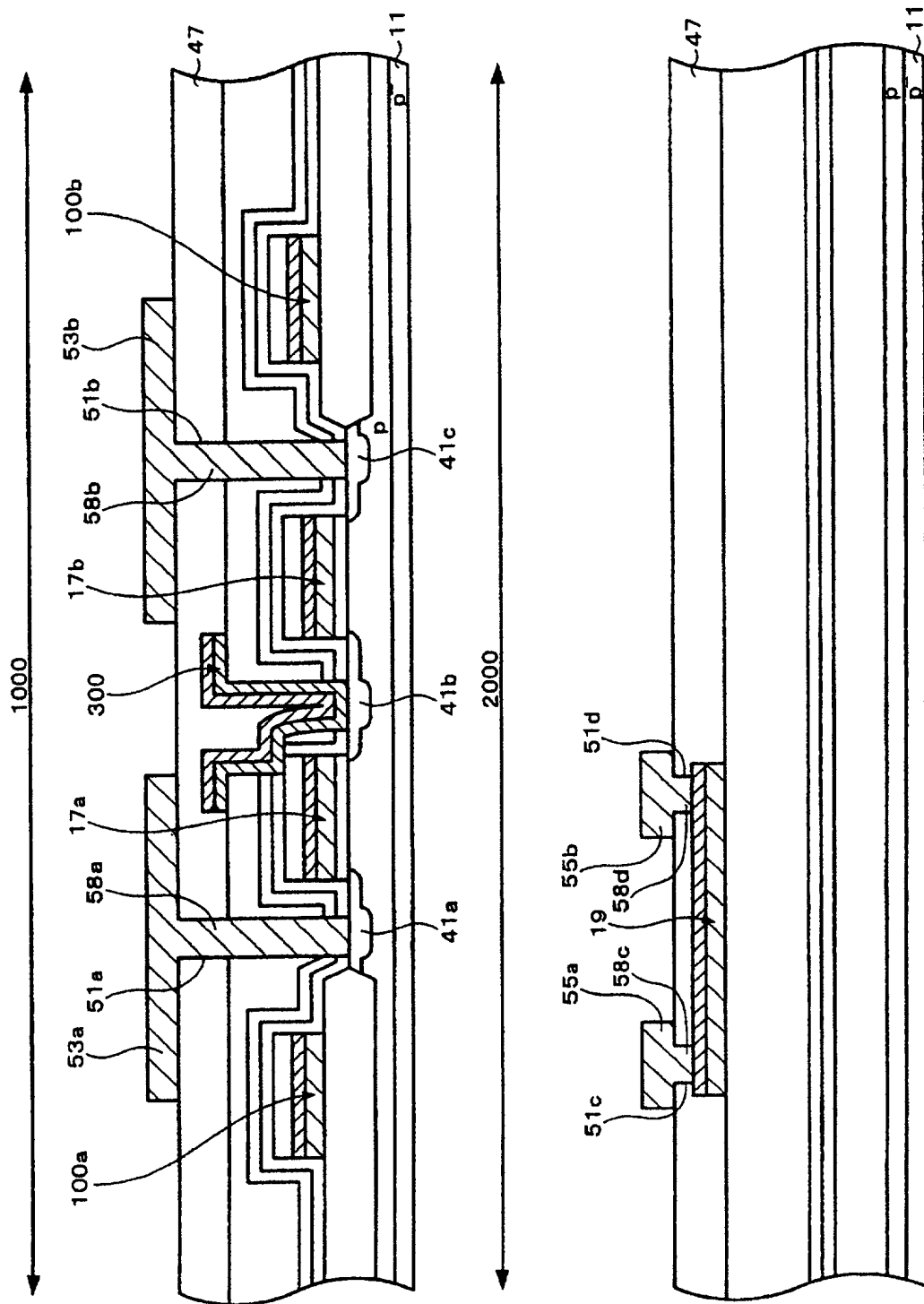


Fig. 7

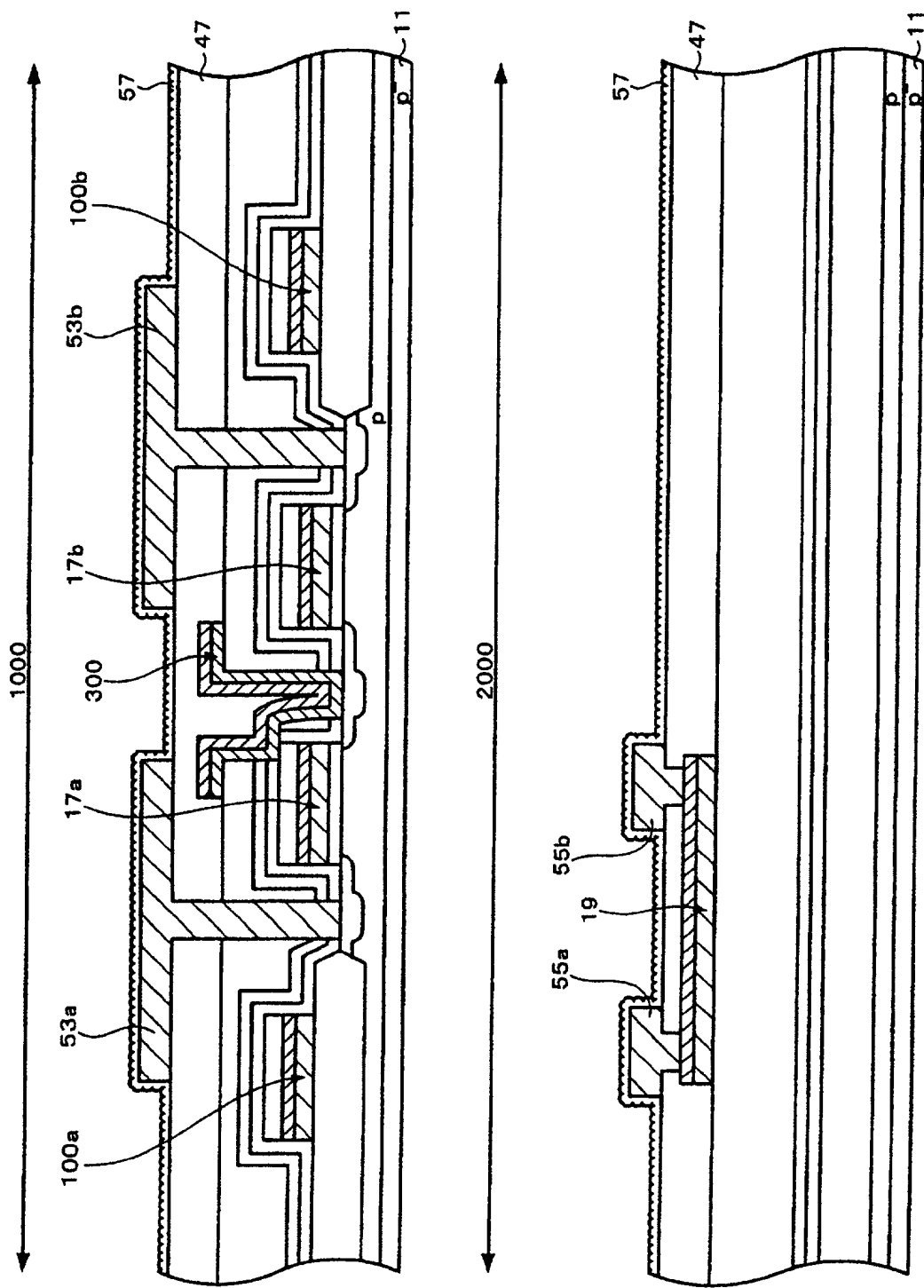


Fig. 8











FIG. 13 is a cross-sectional view of the device of FIG. 12, taken along line 13-13, and showing the device in a second state of operation. In this state, the device is in a second state of operation, and the device is in a second state of operation.

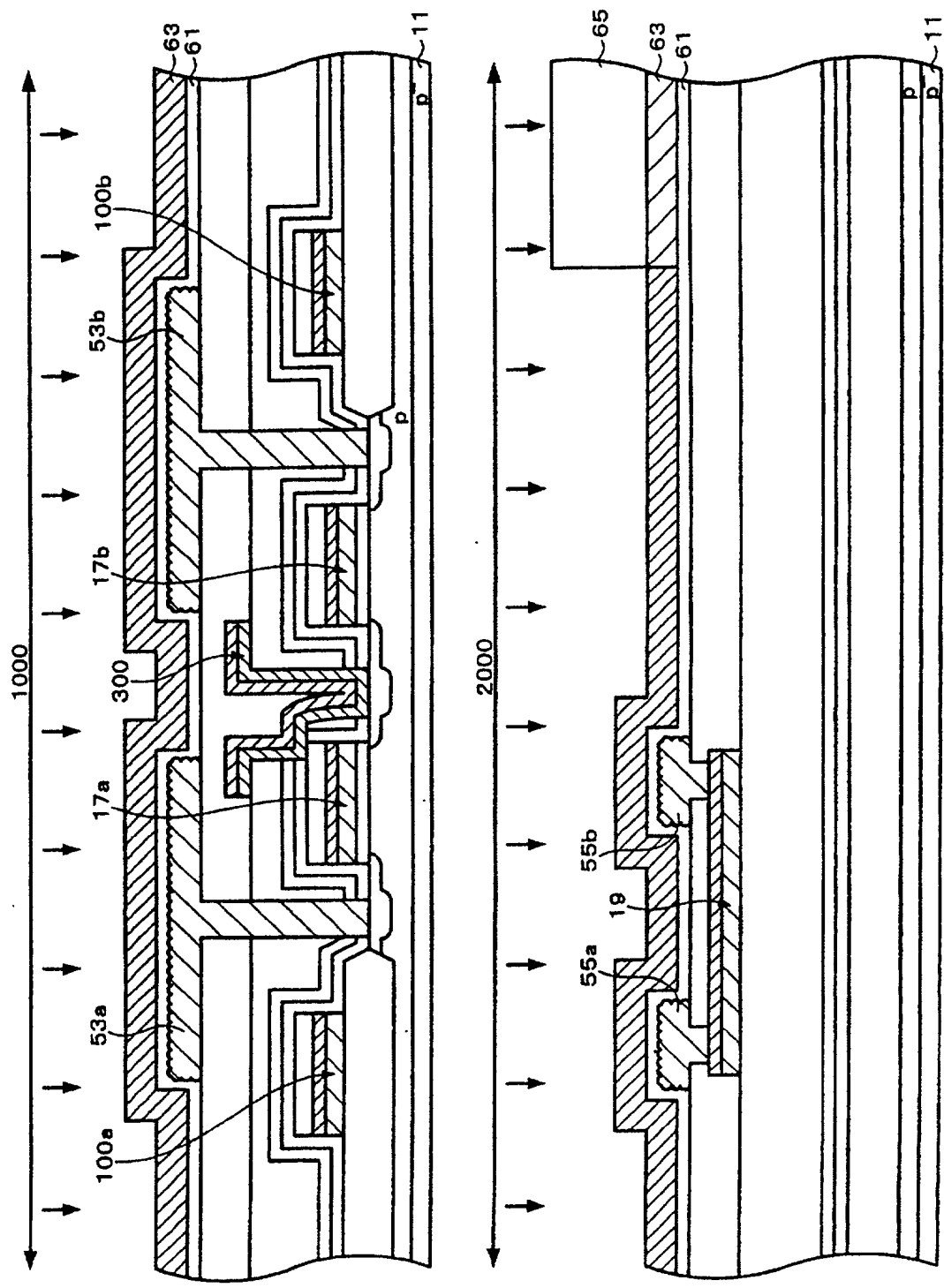


Fig. 13

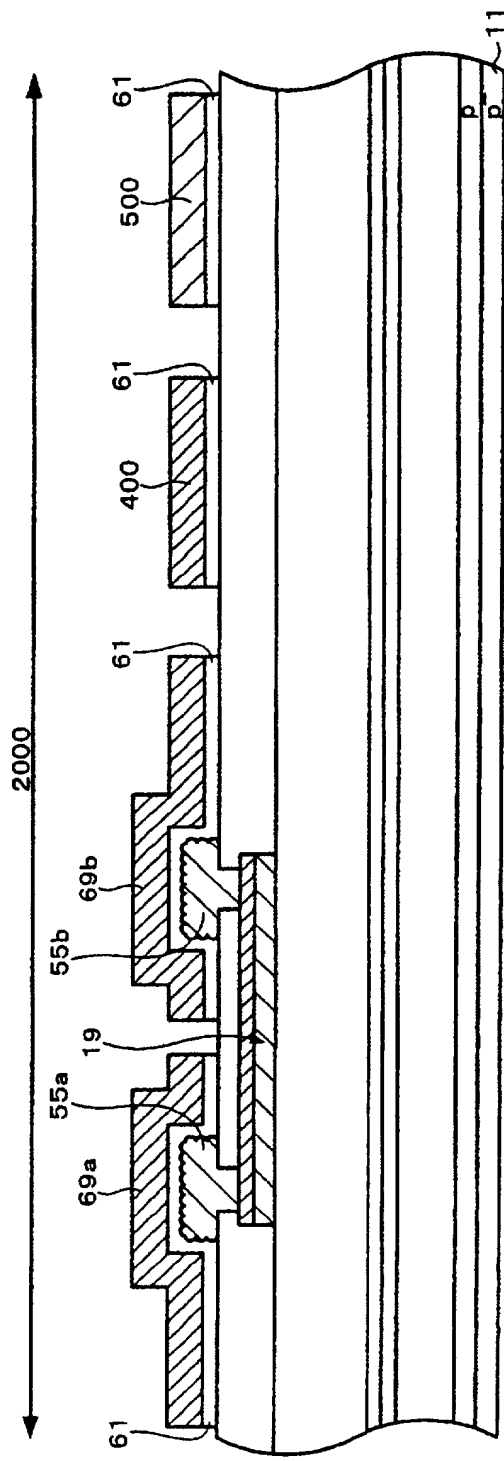
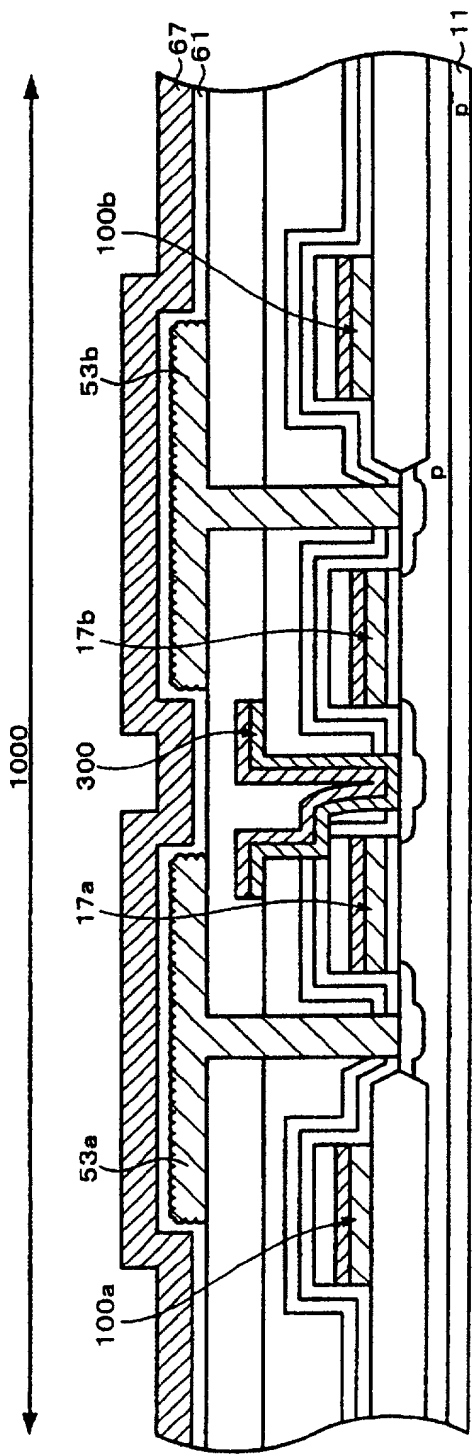


Fig. 14

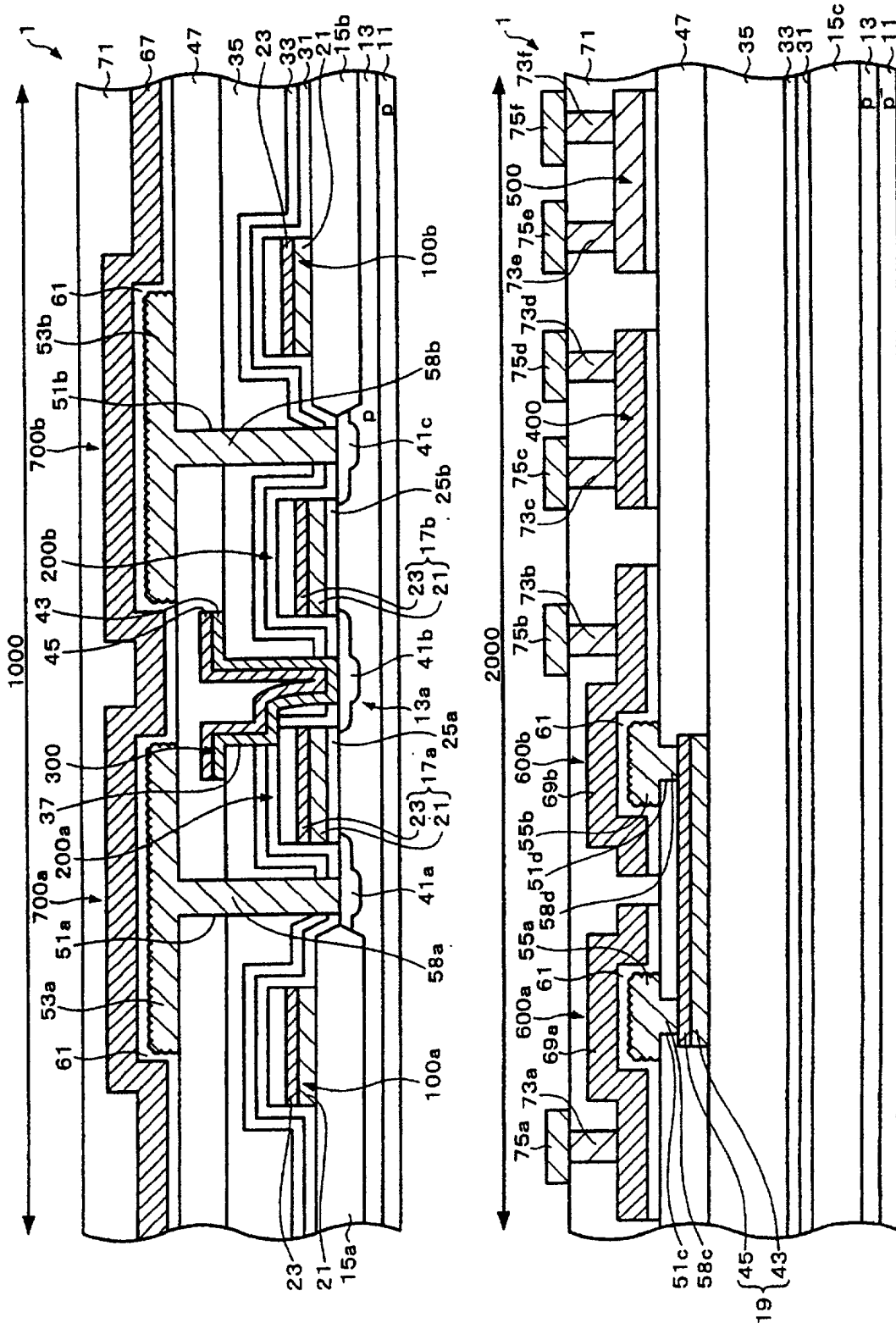


Fig. 15

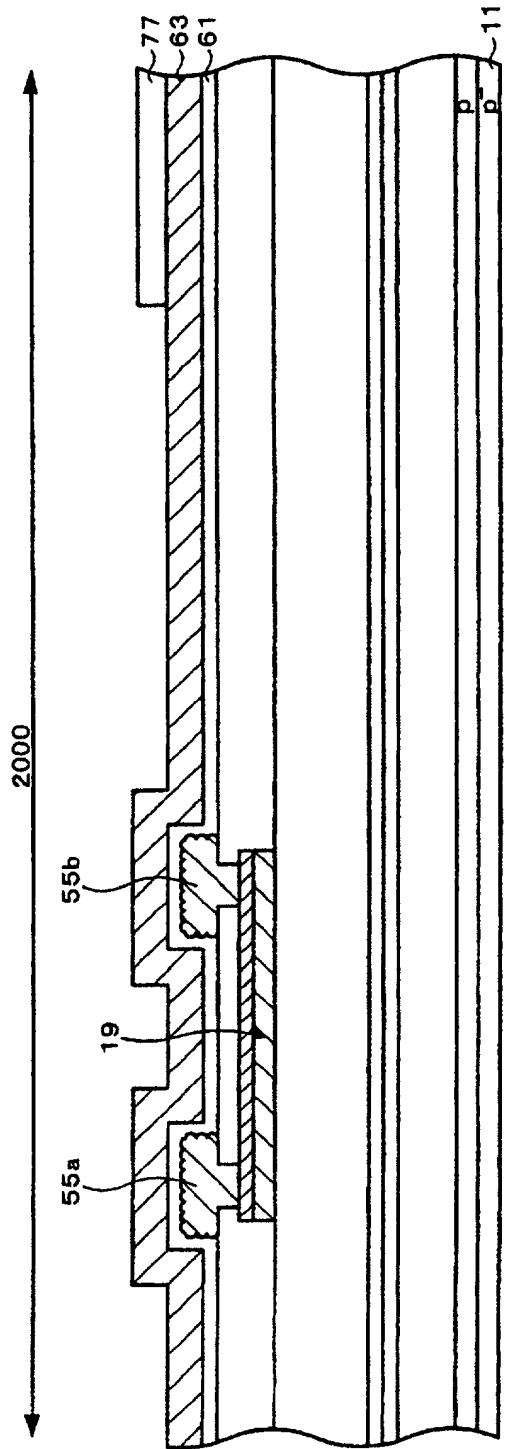
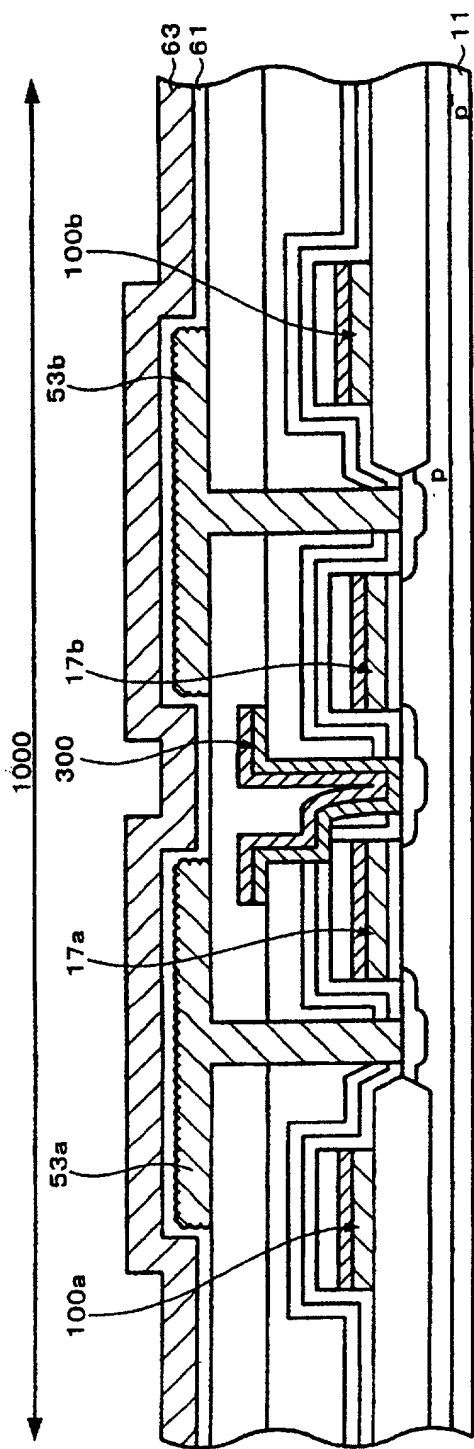


Fig. 16



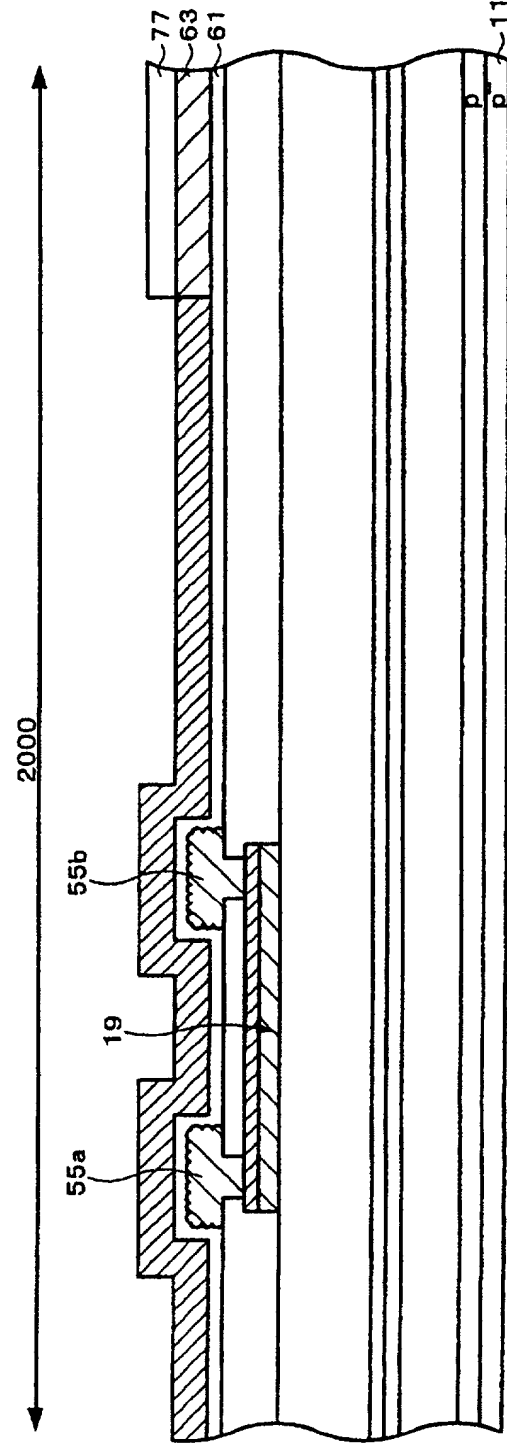
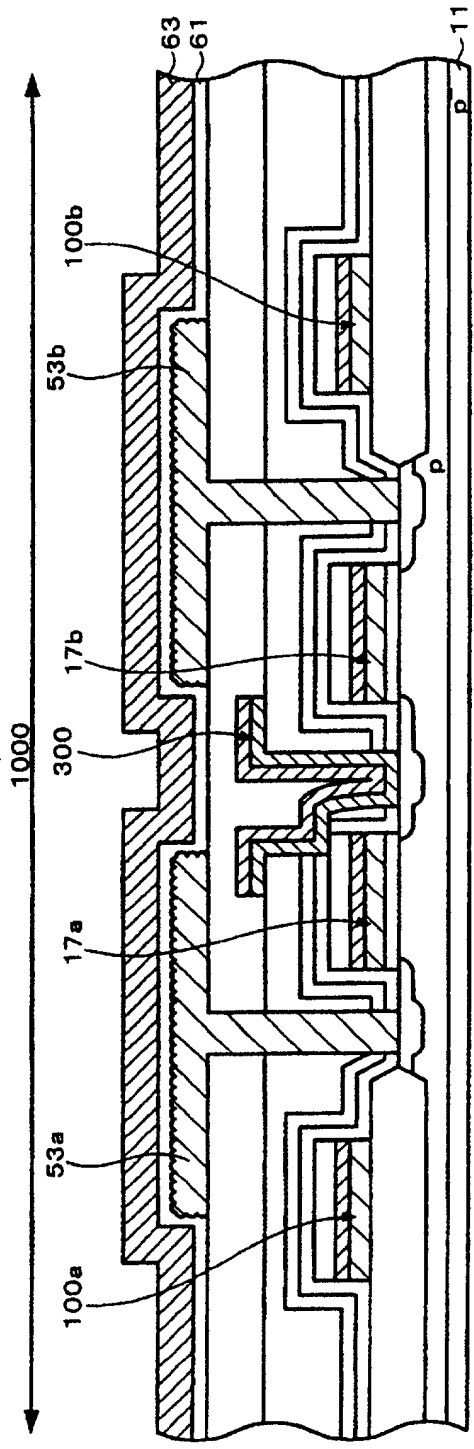


Fig. 17

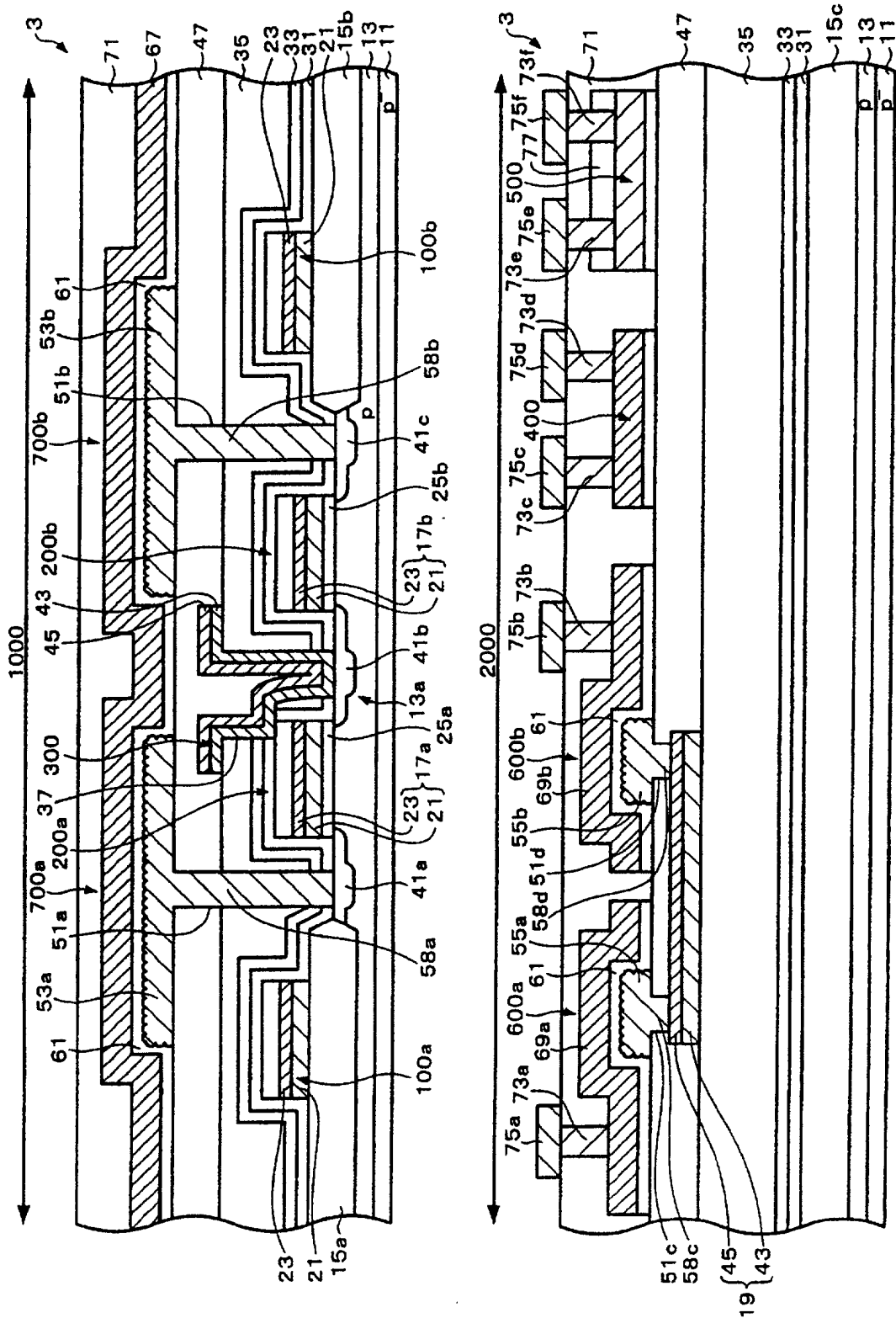


Fig. 18

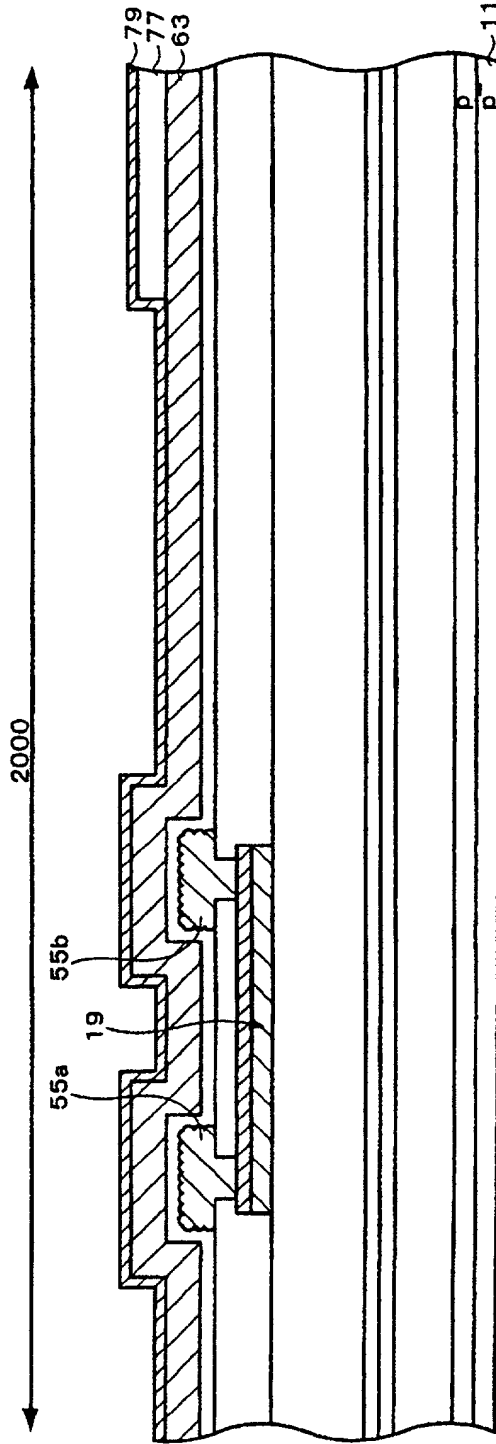
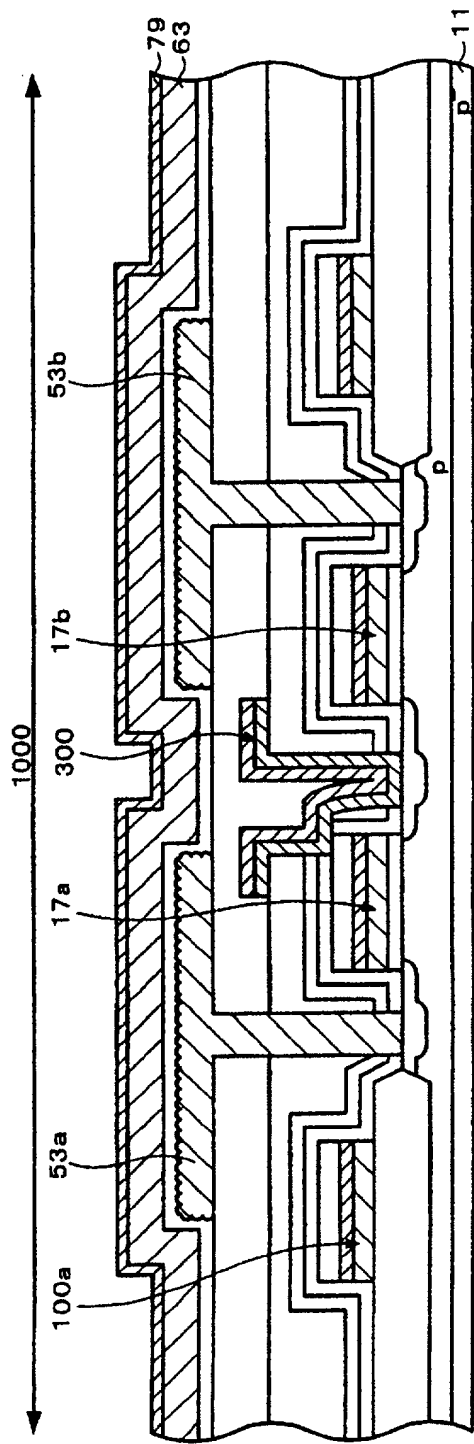


Fig. 19

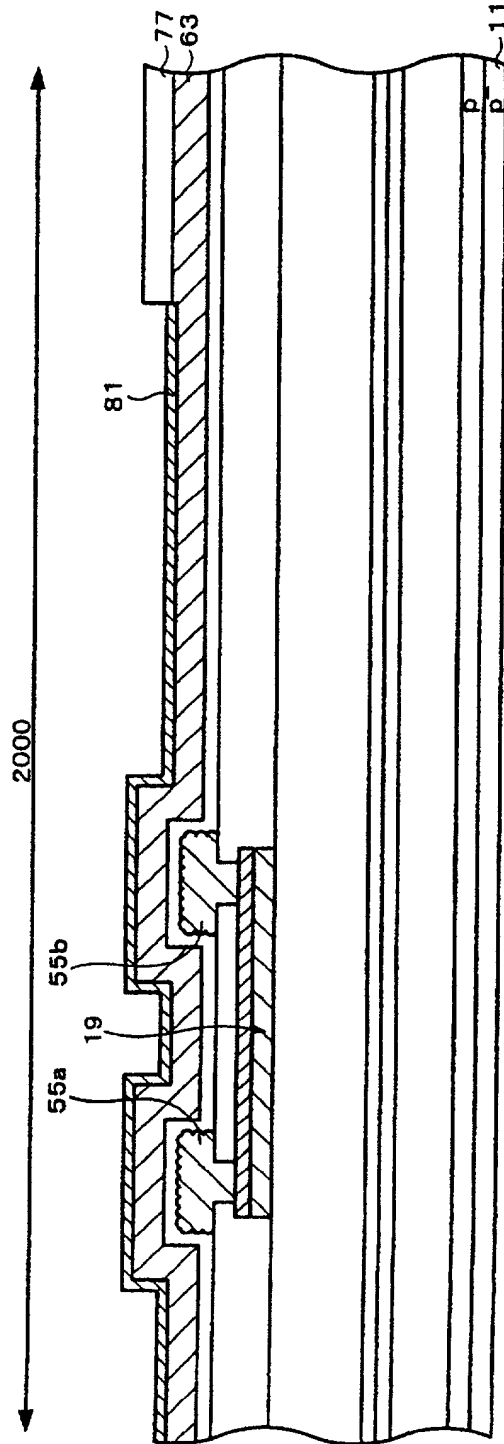
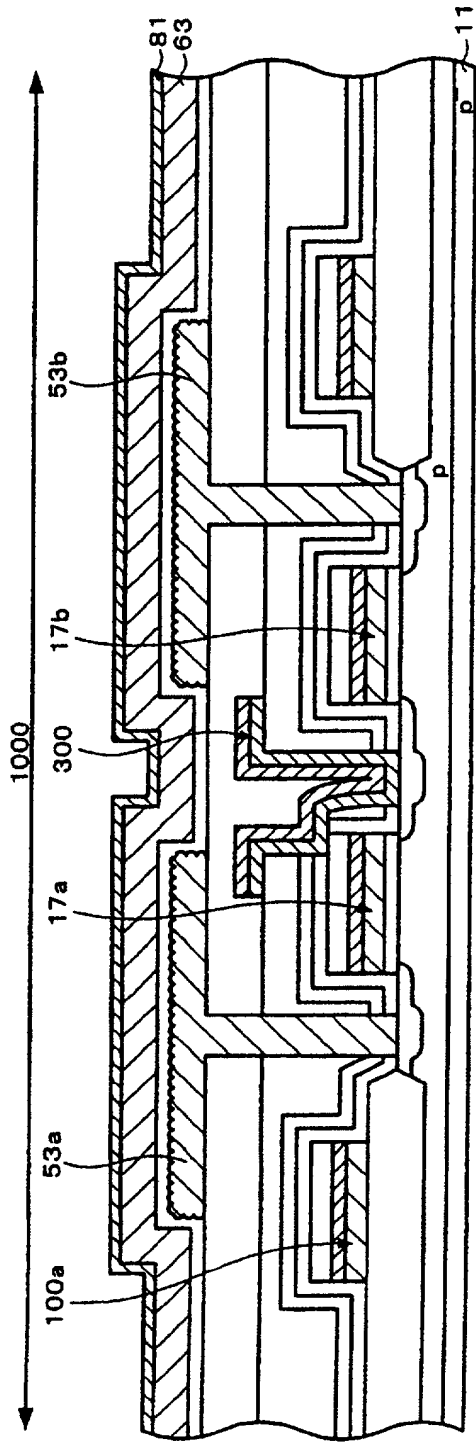


Fig. 20

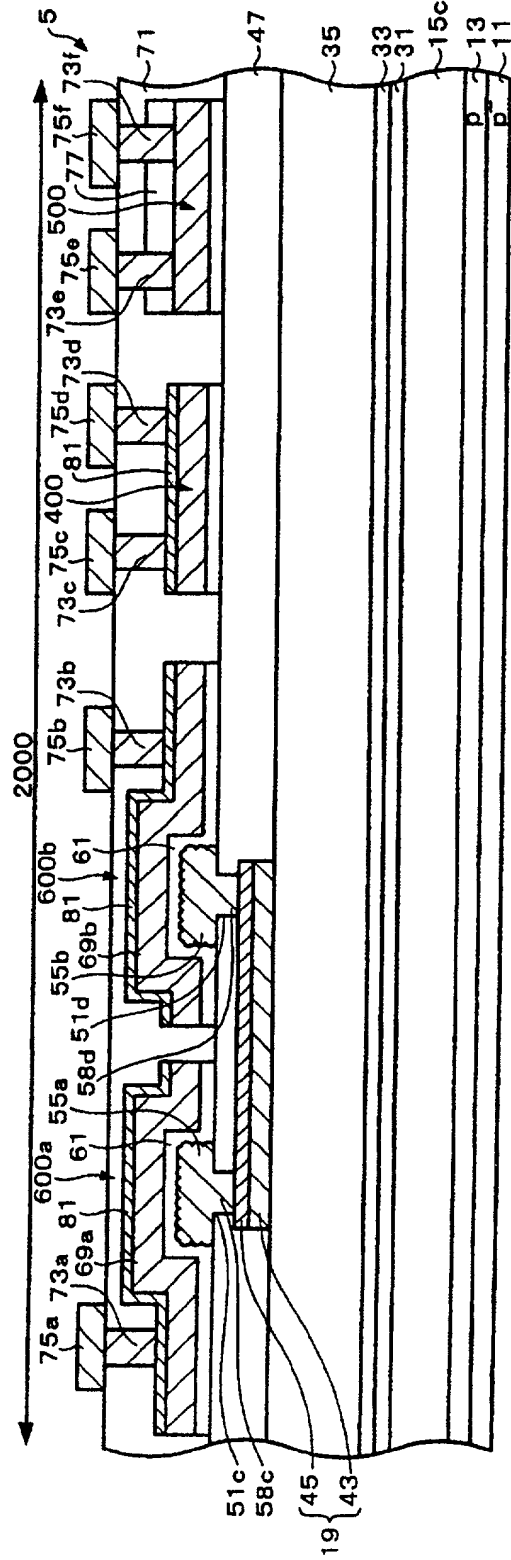
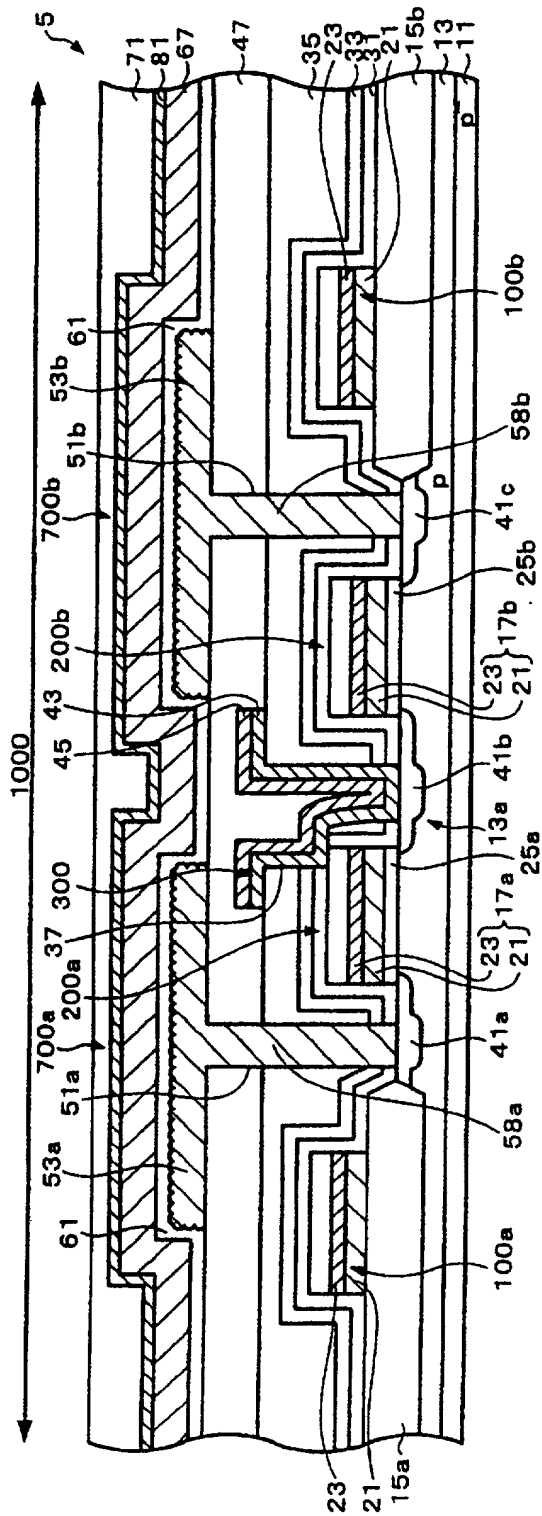


Fig. 21

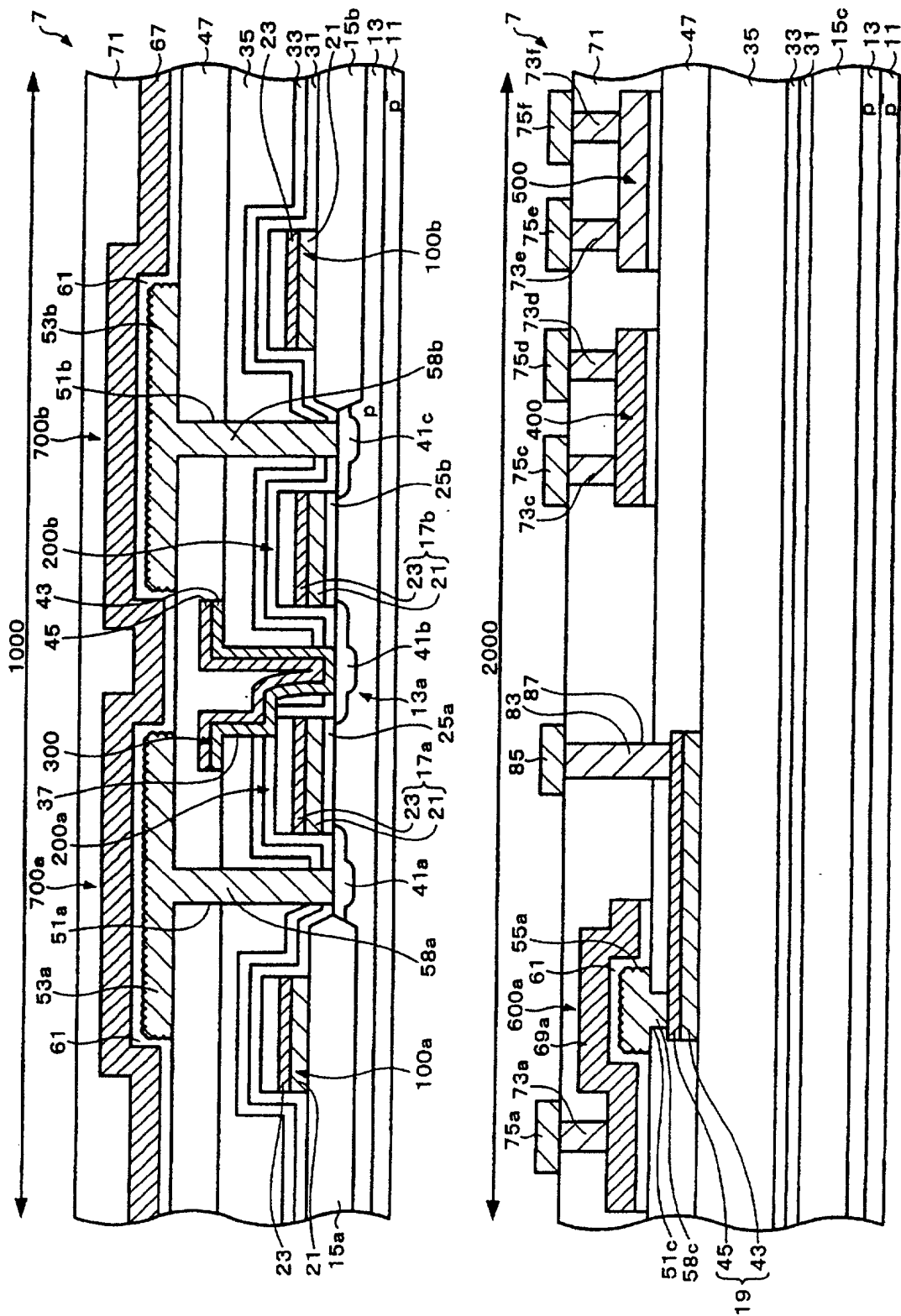


Fig. 22